

FIG. 1

FIG. 2

2/6

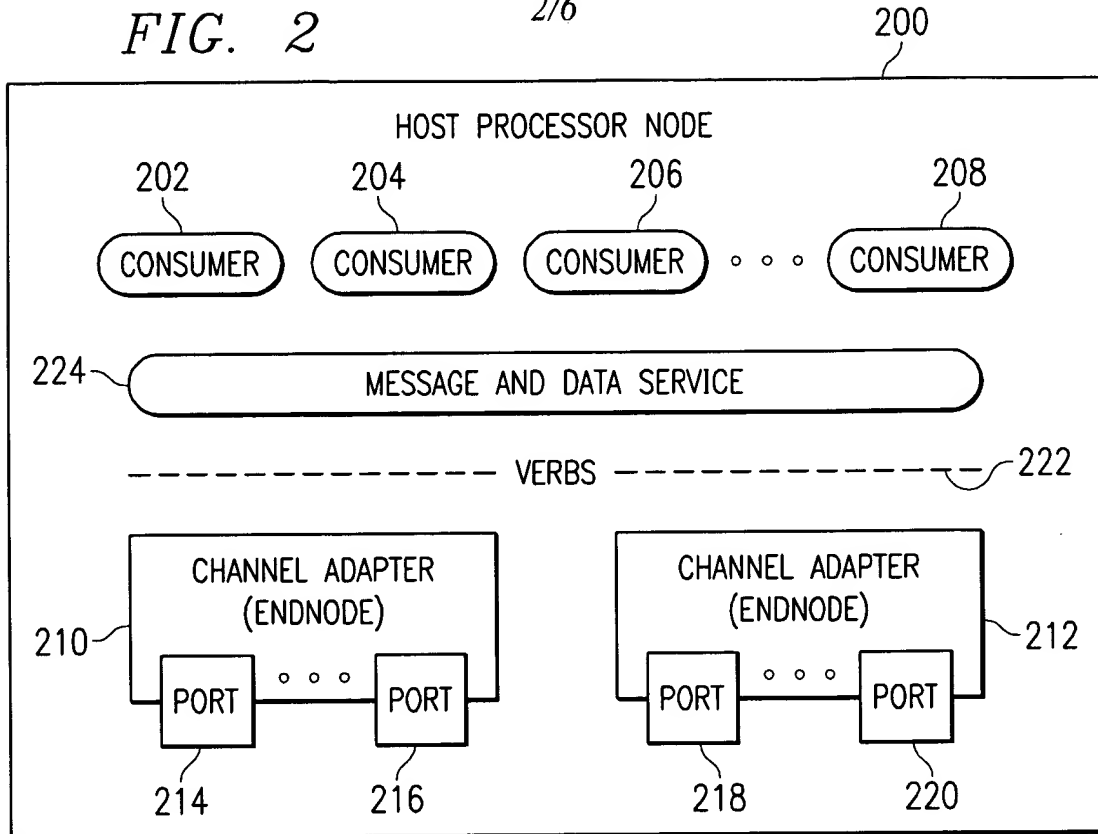


FIG. 3

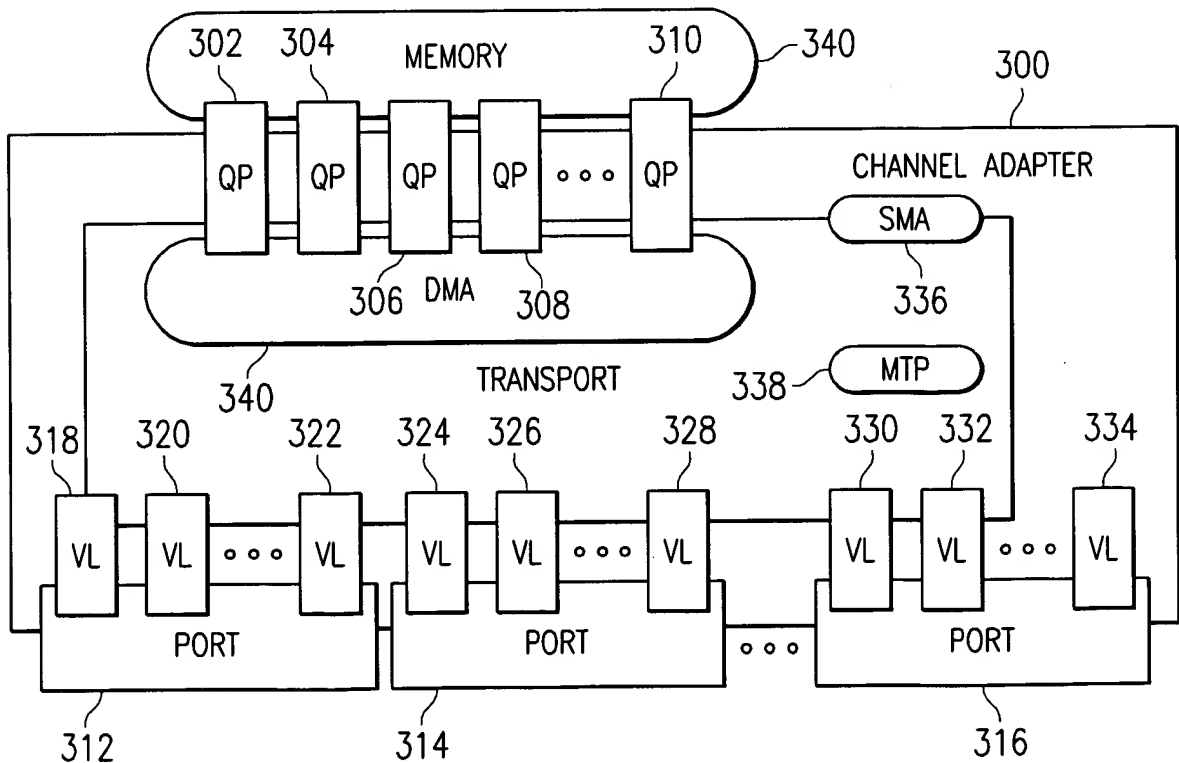
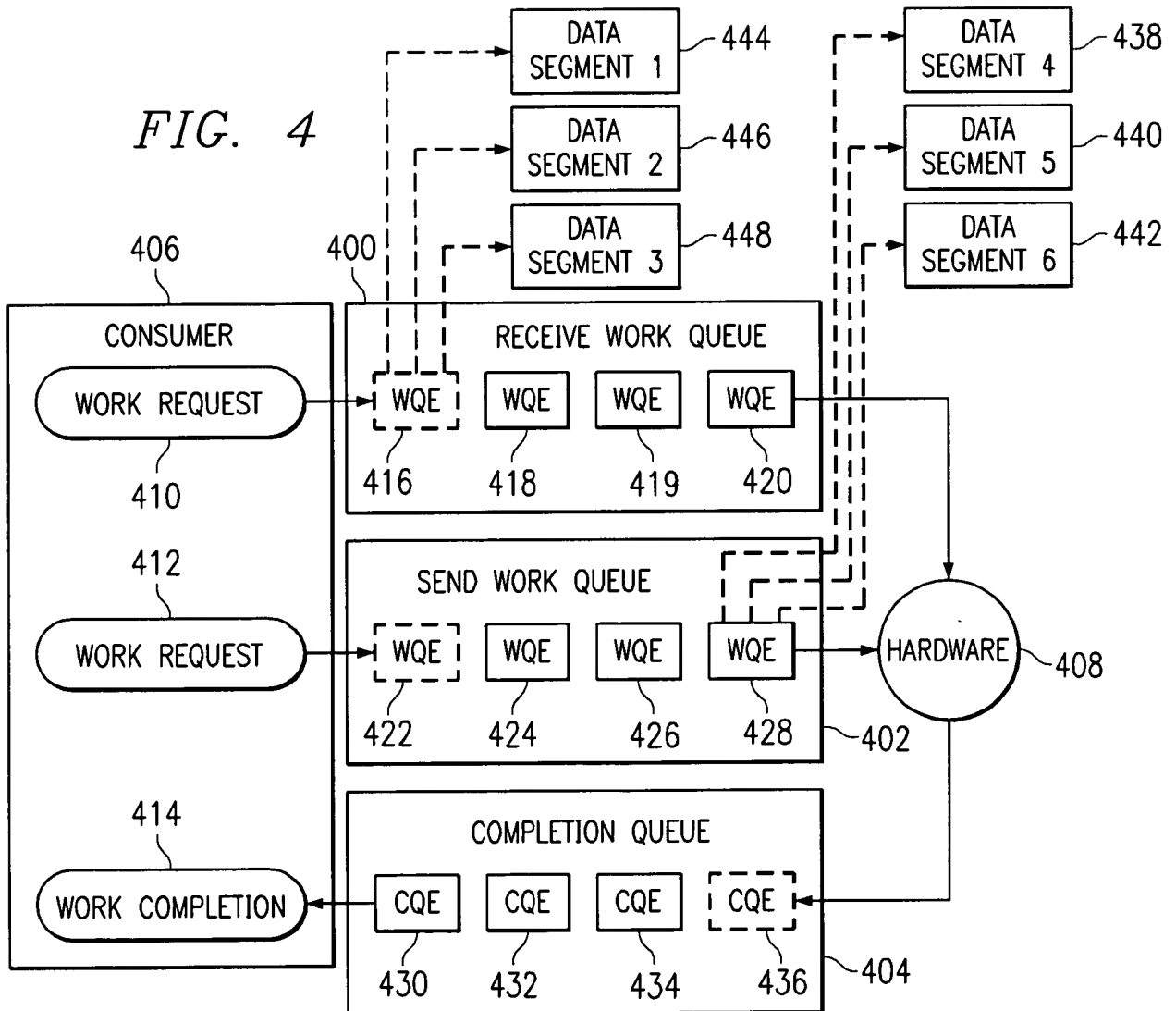


FIG. 4



4/6

FIG. 5

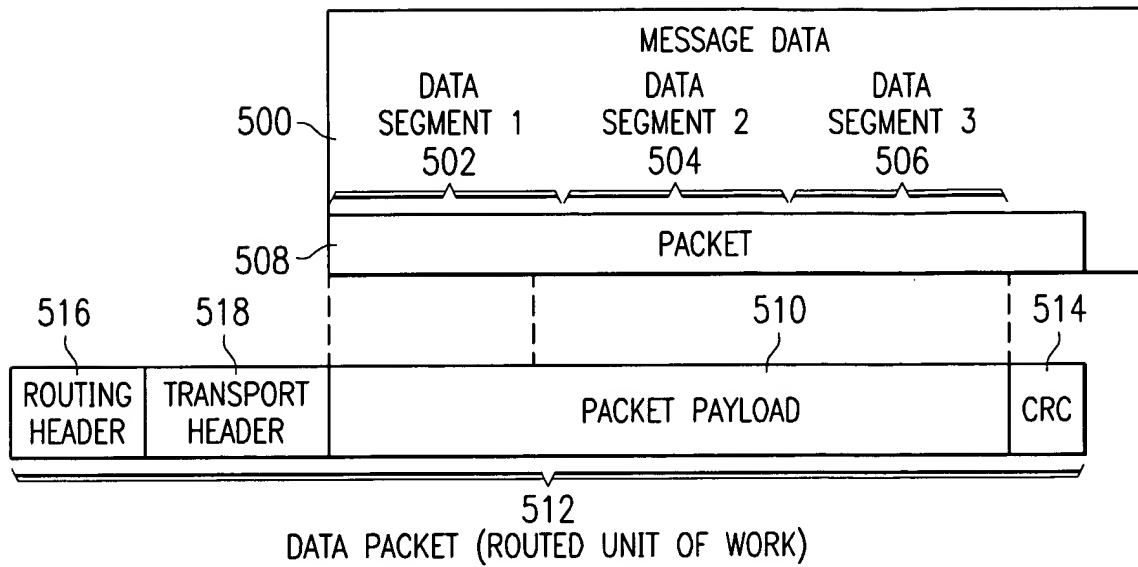


FIG. 6

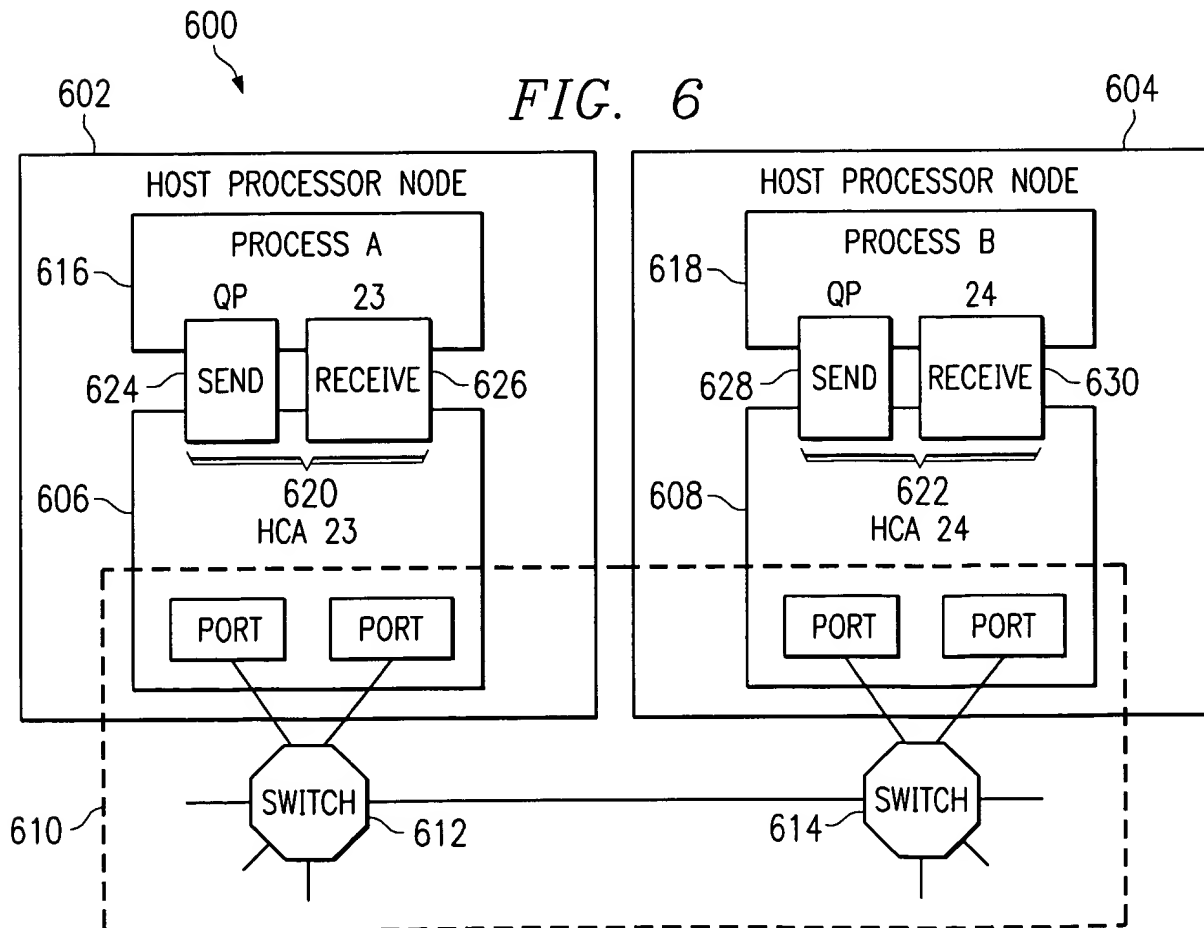
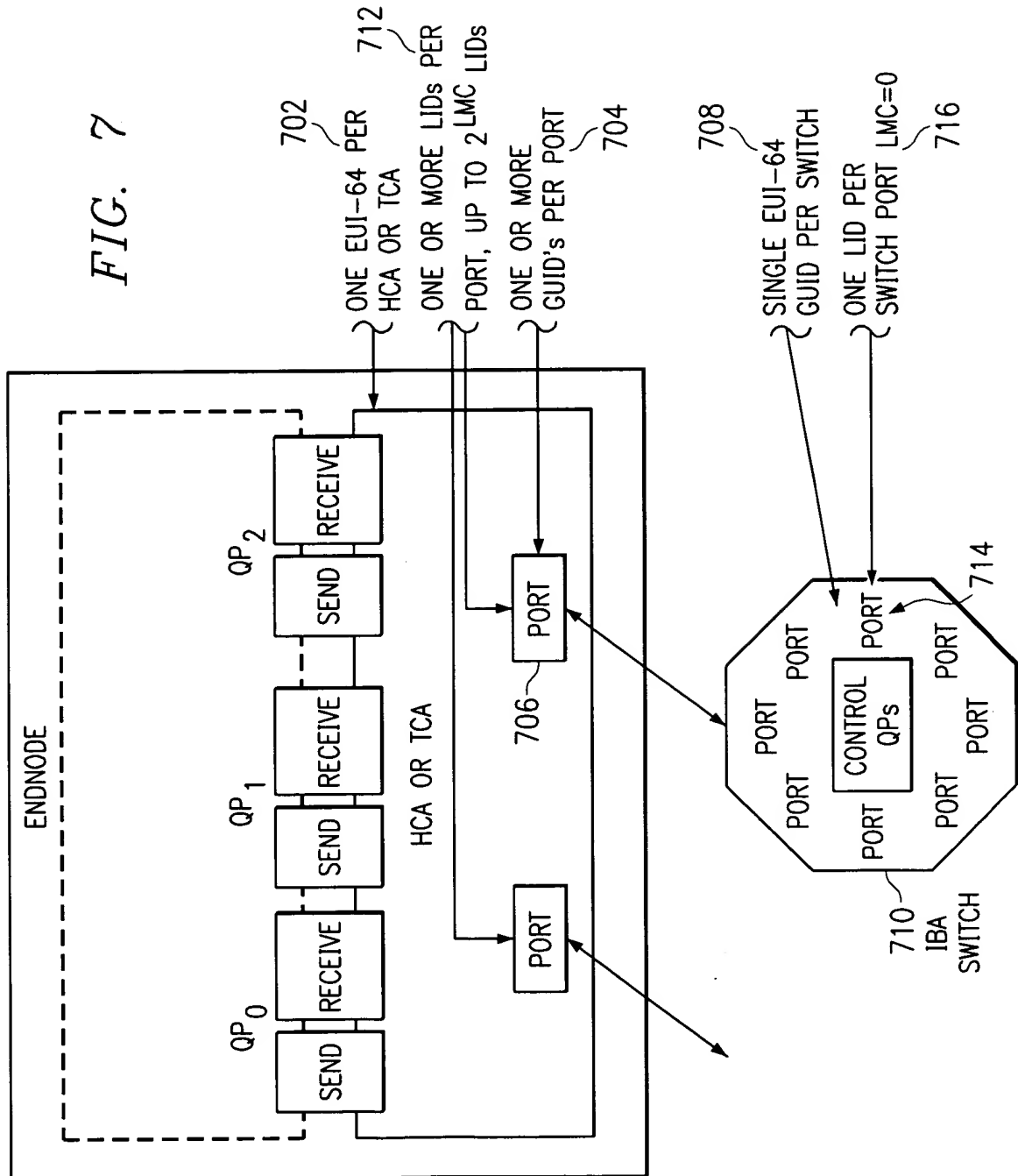
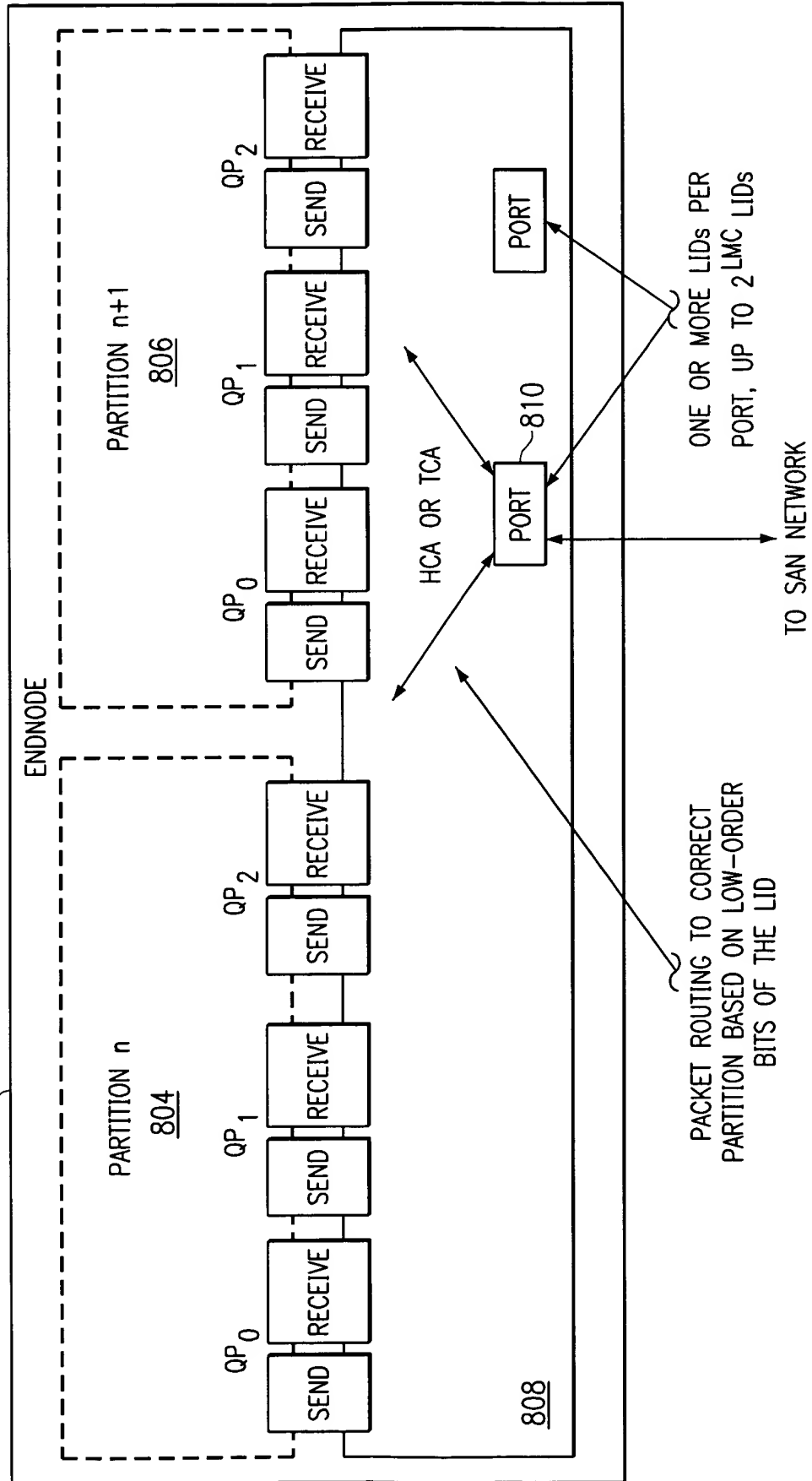


FIG. 7



6/6

FIG. 8



802

PARTITION n
804

PARTITION n+1
806

ENDNODE

QP₀

QP₁

QP₂

QP₀

QP₁

QP₂

SEND
RECEIVE

SEND
RECEIVE

SEND
RECEIVE

SEND
RECEIVE

SEND
RECEIVE

SEND
RECEIVE

HCA OR TCA

PORT

808

PORT 810

PACKET ROUTING TO CORRECT
PARTITION BASED ON LOW-ORDER
BITS OF THE LID

ONE OR MORE LIDS PER
PORT, UP TO 2 LMC LIDS

TO SAN NETWORK